	Application No.	Applicant(s)	
Notice of Allowability			W
	09/891,466 <b>Examiner</b>	DUNNING ET AL.  Art Unit	_(\',
,	Laminer	Artomi	
	Linh M. Nguyen	2816	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS In herewith (or previously mailed), a Notice of Allowance (PTOL-88 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT In of the Office or upon petition by the applicant. See 37 CFR 1.33	S (OR REMAINS) CLOSED in this 5) or other appropriate communica RIGHTS. This application is subje	application. If not included tion will be mailed in due co	urse. <b>THIS</b>
1. This communication is responsive to <u>amendment filed 04</u>	<u>1/18/2005</u> .		
2. The allowed claim(s) is/are <u>1-22</u> .			
3. $\boxtimes$ The drawings filed on <u>27 June 2001</u> are accepted by the	Examiner.	•	
<ul> <li>4. Acknowledgment is made of a claim for foreign priority (a)</li> <li>a) All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 1. Copies of the certified copies of the priority of the prior</li></ul>	ve been received. ve been received in Application No	·	n from the
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	" of this communication to file a re IMENT of this application.	ply complying with the requi	rements
5. A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which gi			TICE OF
6. CORRECTED DRAWINGS (as "replacement sheets") medical including changes required by the Notice of Draftspering 1) hereto or 2) to Paper No./Mail Date	rson's Patent Drawing Review ( P  r's Amendment / Comment or in the	e Office action of	ack) of
7. DEPOSIT OF and/or INFORMATION about the dep attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MATERIA T FOR THE DEPOSIT OF BIOLOG	L must be submitted. No BICAL MATERIAL.	te the
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB Paper No./Mail Date	) 6. Interview Summ Paper No./Mail /08), 7. Examiner's Ame	Date	,

## **DETAILED ACTION**

This is a reply to the Applicant's amendment submitted on 04/18/2005. According to this amendment, claims 1-22 are currently presented in the instant application.

## Allowable Subject Matter

1. Claims 1-22 are allowed.

## Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

The closest prior art fails to disclose or fairly suggest:

- a) A phase interpolator having a plurality of switching legs, each including a tail coupling the other terminal of the switching transistors directly to ground without bias, the tail made up of a plurality of transistors, as called for in claim 1;
- b) A phase interpolator, in which each of the tails comprises four transistors in parallel to make available 16 phases of the reference clock and its complement, as called for in claim 3,
- c) A phase interpolator including an N bit digital control, where N equals the total number of transistors in tails, the digital control having N outputs coupled to respective gates in the transistors in the tails, as called for in claim 4,
- d) A phase interpolator, in which switching transistors are large enough to load the input clocks such that they have a rise and fall times that are equal to or larger than one quarter of an input clock period, as called for in claim 8;
- e) A phase interpolator, to interpolate between four input clock phase signals having four switching legs, each including a pair of differential switching transistors each having a gate,

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the respective gates coupled respective ones of a plurality of clock phases and their complements, each differential switching transistors coupled to a respective one of the common output line and the common complemented output line, in combination with the remaining claimed limitations as called for in claim 10; and

f) A tracking receiver, in which a remote clock recovery mechanism including an N bit digital control, where N equals the total number of transistors in the tails, the control receiving a control input from the phase and frequency having N outputs coupled to respective gates in the transistors in the tails, in combination with the remaining claimed limitations, as called for in claim 16.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Linh M. Nguyen whose telephone number is (571) 272-1749. The examiner can normally be reached on Alternate Fri, Monday - Thursday from 7:00 to 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (571) 272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Linh M. Nguyen Primary Examiner Art Unit 2816

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